

## USBHS-OTG\_ES

USB High Speed On-The-Go Controller Evaluation System

### C68000 FEATURES

- ◆ Motorola® MC68000 compatible
- ◆ Fast 32-bit synchronous memory interface
- ◆ 32-bit internal architecture
- ◆ 4 GB of linear addressing space
- ◆ Supervisor and user operation modes with separate stacks

### EB-5 TINY SETUP

- ◆ EB5-Spartan3 adapter with the Xilinx® Spartan3 FPGA and the JTAG input for debug interface
- ◆ SMSC 3300 USB OTG transceiver
- ◆ USB mass storage devices (pendrive and HDD)
- ◆ 4 MB of SRAM, 8 MB of SDRAM and 2 MB of Flash memory
- ◆ One of each: PS-2, RS-232, and CF connectors
- ◆ User interface with 8 LEDs, 1 LCD display (4x20 pixels), 6 push buttons, and a keyboard

### SOFTWARE SETUP

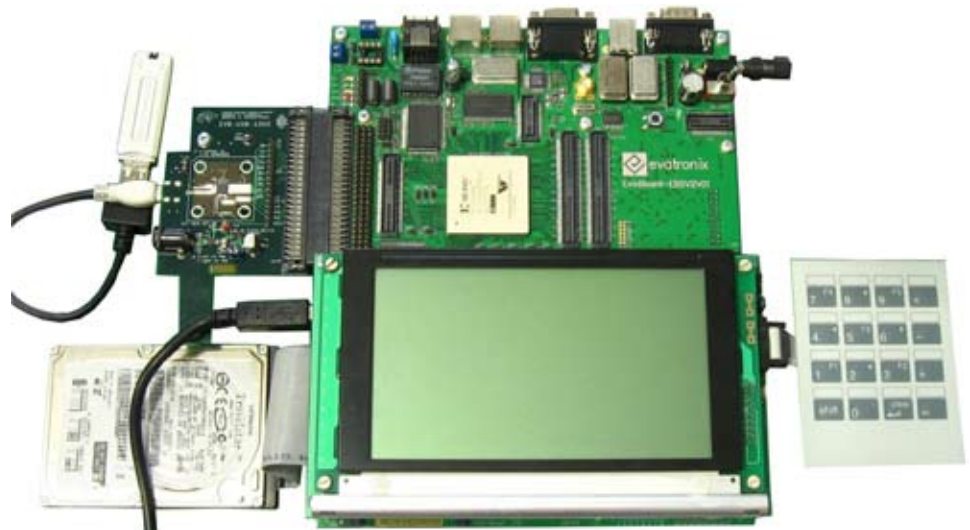
- ◆ USB-OTG device with HDD mass storage memory with convenient GUI for file management
- ◆ Micrium® uC/OSII for multitasking capabilities

### OVERVIEW

The main purpose of the **USBHS-OTG\_ES** evaluation system is to demonstrate the functionality of the MPD version of the USB 2.0 High Speed On-The-Go controller running in a real microprocessor application.

This system allows development of more sophisticated software or even whole applications that will make use of the **USBHS-OTG IP**.

The system is based on a proprietary **EB5-Tiny** evaluation board, in which a Xilinx® Spartan-3 FPGA is implemented with the **C68000** microprocessor and the **USBHS-OTG** USB 2.0 High Speed On-The-Go controller in it. The system contains all the software necessary to download and debug the user's application.



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