

# T8051\_ES

The World's Smallest 8051 Microcontroller Evaluation System

## EB-5 TINY SETUP

- ◆ EB5-Spartan3 adapter with the Xilinx® Spartan3 FPGA and the JTAG input for debug interface
- ◆ FPGA Adapter with a Xilinx® Spartan3-1500 device
- ◆ EB5 Peripheral adapter containing Flash and EEPROM memories, thermometer and thermostat, SD/MMC card connector and UTMI connector

## SOFTWARE SETUP

- ◆ Philips LPC2000 Flash Utility used to update EDP software placed in FLASH memory
- ◆ **EASE-8051** - KEIL™-compatible, complete application debugging environment
- ◆ Embedded demo application

## OVERVIEW

The main purpose of the **T8051\_ES** is to demonstrate functionality of the **T8051** IP core. The **T8051** virtual component is a low gate count single-chip 8 bit microcontroller which executes ASM51 instruction set.

The base of this evaluation system are Evatronix hardware and software sets. Former consists of evaluation board with Xilinx FPGA device that can be programmed to implement **T8051** functionality other most important part is EDP device used for debugging and software loading purposes.

Hardware is supported by specialized software like EDIk51-2 that services as an interface between Keil™ µVision debugger and EDP device that generates JTAG compliant signals. All these elements mentioned above connected together create complete evaluation system.

