

R8051XC2_ES

The World's Fastest 8051 Microcontroller Evaluation System

EB-5 TINY SETUP

- ◆ EB5-Spartan3 adapter with the Xilinx® Spartan3 FPGA and the JTAG input for debug interface
- ◆ FPGA Adapter with a Xilinx® Spartan3-1500 device
- ◆ EB5 Peripheral adapter containing Flash and EEPROM memories, thermometer and thermostat, SD/MMC card connector and UTMI connector

SOFTWARE SETUP

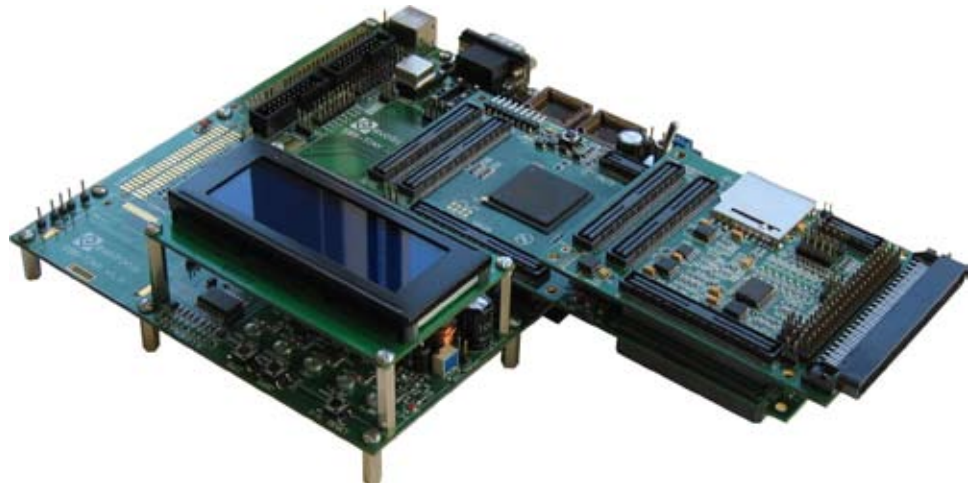
- ◆ Drivers for **EB5-Tiny** I²C and SPI peripheral devices
- ◆ **EASE-8051** - KEIL™-compatible, complete application debugging environment
- ◆ R8051XC2 demo application with drivers for UART, TIMER, I2C and SPI peripherals

OVERVIEW

The main purpose of the **R8051XC2_ES** is to demonstrate the functionality of the **R8051XC2** virtual component running in real microcontroller application.

The **R8051XC2_ES** allows an easy start with the **R8051XC2** core and facilitates further application development.

The evaluation board contains an FPGA device in which the **R8051XC2** microcontroller core is implemented, as well as a variety of peripheral devices such as an LCD display, two user buttons, 8 user LEDs and an SD/MMC adapter. Using these devices, user is able to interact with the **R8051XC2** microcontroller. Provided software enables to download and run user application in the **R8051XC2** microcontroller.



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