

C80186_ES

Intel® 80186-compatible Microcontroller Evaluation System

EB-5 TINY SETUP

- ◆ EB5-Spartan3 adapter with the Xilinx® Spartan3 FPGA and the JTAG input for debug interface
- ◆ 2 MB of SRAM and 1 MB of Flash memory
- ◆ One of each: PS-2, RS-232, HDD and CF connectors
- ◆ User interface with 8 LEDs, 1 LCD display (4x20 pixels), and 2 push buttons

SOFTWARE SETUP

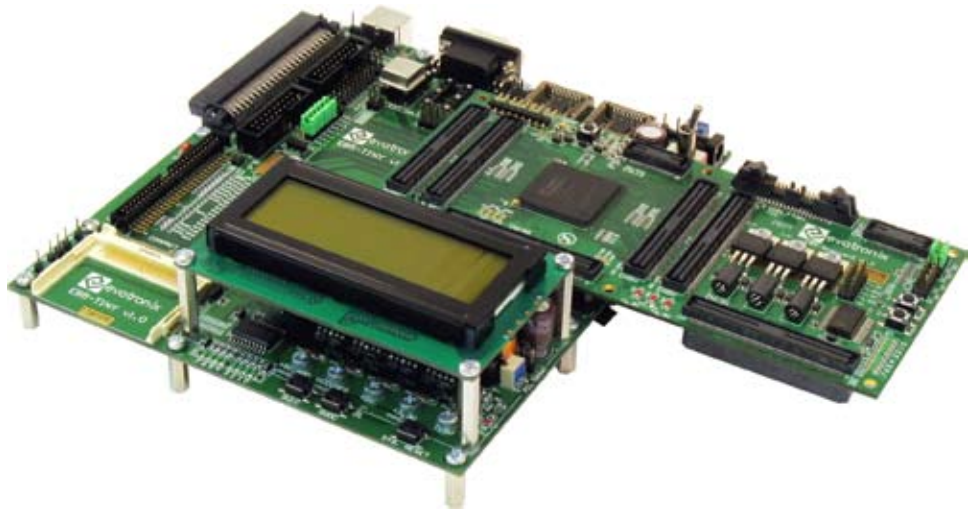
- ◆ C80186XL software project for the PACIFIC computer

OVERVIEW

The main purpose of the **C80186_ES** evaluation system is to demonstrate the functionality of the C80186XL, the most complex version of the Intel® 80186-compatible microcontroller IP running a real-world application.

This system allows development of more sophisticated software or even whole applications that will make use of all versions of the **C80186** IP.

The system is based on a proprietary **EB5-Tiny** evaluation board, in which a Xilinx® Spartan-3 FPGA is implemented with the **C80186XL** microcontroller in it. The system contains all the software necessary to download and debug the user's application.



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