

C80186

CONFIGURABILITY

A rich list of **C80186** compatible peripherals that can be provided includes the following: UART, USART, Refresh unit, DRAM control, DMA, Interrupt controller, 8259a, Timer/Counter, 8/16-bit IO, WatchDog.

These peripherals allow development of the following 80186/80188 family variations:

- ◆ Intel® 80c186ec/80c188ec
- ◆ Intel® 80c186xl/80c188xl
- ◆ Intel® 80c186ea/80c188ea
- ◆ AMD® AM186ER
- ◆ AMD® AM186ED
- ◆ AMD® AM186ES

BENEFITS

- ◆ A rich set of available peripherals makes C80186 adaptable to almost any Intel or AMD version of 80186 microcontroller
- ◆ Having the IP core of the processor guarantees independence from the chip vendor
- ◆ 100% compatibility with the original chip enables Plug-and-Play-like portability to various applications
- ◆ Obsolete IP cores, on contrary to original chips, can be easily integrated in SoC designs targeted to FPGA or ASIC technologies

PRODUCT VERSIONS

C80186EC – an Intel® 80c186ec pin compatible version of the C80186 microcontroller. It implements a DRAM Refresh unit, 3x Timer/Counter, 2x USART, 4x DMA, 3x 8-bit IOs, 2x 8259a, and a WatchDog.

C80186XL – an Intel® 80c186xl pin compatible version of the C80186 microcontroller. It implements a DRAM Refresh unit, 3x Timer/Counter, 2x DMA, and an Interrupt Control Unit.

C80186EA – an Intel® 80c186ea pin compatible version of the C80186 microcontroller. It implements a DRAM Refresh unit, 3x Timer/Counter, 2x DMA, and an Interrupt Control Unit.

All of the products above can also be delivered as 80c188 compatible 8-bit systems.

APPLICATIONS

- ◆ High speed control systems
- ◆ Microcomputer systems
- ◆ Automotive control
- ◆ Audio and video control

STANDARD DELIVERABLES

- ◆ VHDL/Verilog source code
- ◆ Synthesis support for Synopsys® tools with a set of synthesis scripts
- ◆ Simulation support for Mentor Graphics® and Cadence® tools with a set of scripts and macros
- ◆ Extensive VHDL/Verilog 2001 test bench
- ◆ Documentation:
 - ▶ Design Specification
 - ▶ Verification Specification
 - ▶ Integration Manual
- ◆ 30 days of technical support
- ◆ 90 days of warranty against defects

DELIVERY OPTIONS

- ◆ EDIF netlist for FPGA and low volume production
- ◆ Evaluation system for proprietary **EB5-Tiny** board
- ◆ One-year maintenance
- ◆ On-site support and training



For more information on our IP portfolio visit www.evatronix-ip.com



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